

CLK	V/O	R0T	SCA
TRG	TG2	TG5	
PI1	PI2	PI3	

I1	Clock	01	trigger
I2	optional: external pitch	02	trigger delayed by 2 clocks
I3	optional: root note	03	trigger delayed by 5 clocks
I4	optional: scale (degree)	04	
I5		05	pitch note 1
I6		06	pitch note 2
I7		07	pitch note 3
I8		08	

